



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2183
A7/A
1-23-04
DW

Application of: Rene Vangemert et al.

Serial No.: 09/729,508

Title: CPU LOAD-MISS RECOVERY MECHANISM

Filed: December 4, 2000

Attorney Docket No.: 00-487 / 1496.00053

Examiner: Gerstl, S.

Art Unit: 2183

In Response To: Office Action mailed October 21, 2003

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Technology Center 2100

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 12, 2004.

By:

Mary Donna Berkley
Mary Donna Berkley

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed October 21, 2003
please amend the above-identified application as follows: